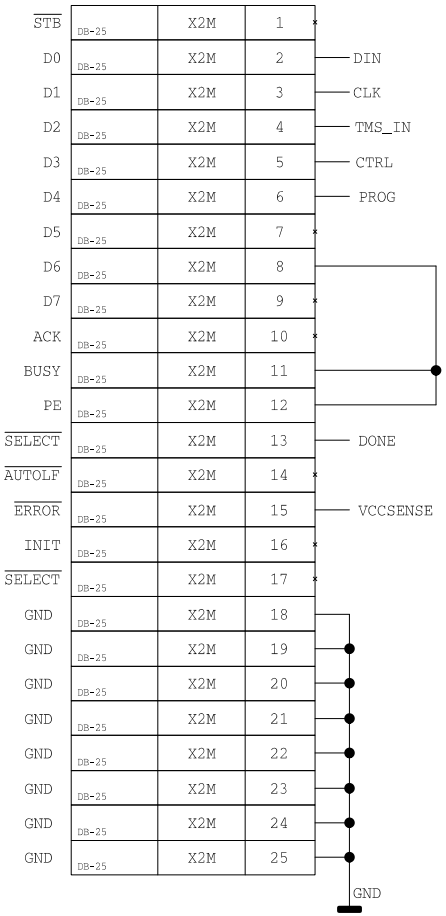

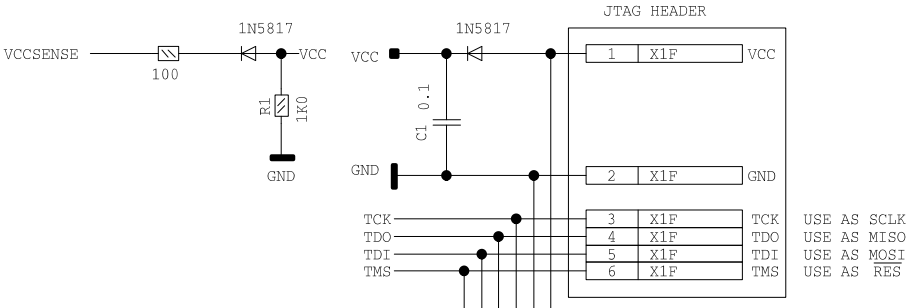

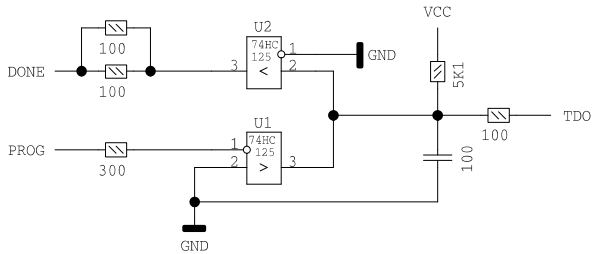

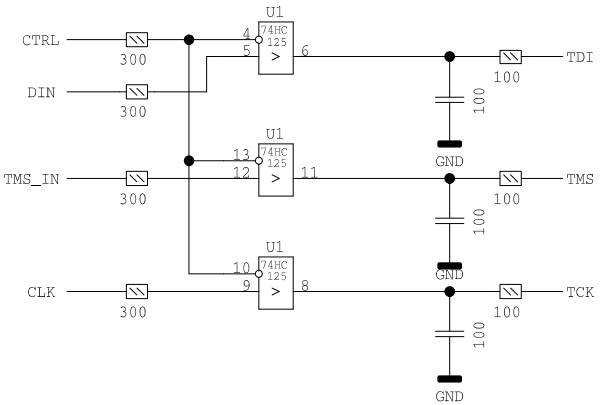

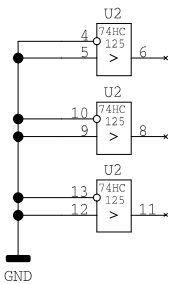

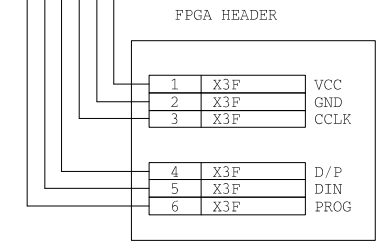

			XILINX CABLE ver.2	
		DATA		
DESIGNER	Inchagov	19.03.04	JTAG/FPGA	SHEETS 1
LAB. CHIEF				SHEET 1